

AMENDMENTS TO THE CLAIMS

This listing of claims will replace all prior versions and listings of claims in the application:

LISTING OF CLAIMS:

1. (Currently Amended) A Reconfigurable multiplexer for a wireless transceivers comprising transceiver, wherein said reconfigurable multiplexer comprises a manifold (MF), filter ports and filter means, with each filter means being to be connected to the manifold (MF) at a corresponding one of said ports proper locations (P1, P2, ... P5), wherein characterized in that at least one of said filter means comprises:

at least one filter comprising a first resonant cavity and a further resonant cavity, and at least one filter head (FHD1, FHD2, ... FHD5) having only a single resonant cavity which is the same as the first resonant cavity of said at least one filter, wherein said at least one filter head is configured as to be connectable either to a corresponding covering plate (SC1, SC2, ... SC5) for short circuit purposes or to a filter tail (FTL1, FTL2, ... FTL5) in order to provide full filter functionality.

2. (Currently Amended) A Reconfigurable multiplexer according to claim 1, characterized in that wherein the at least one filter head (FHD1, FHD2, ... FHD5) comprises at least a first coupling and in addition to said first resonant cavity.

3. (Currently Amended) A Reconfigurable multiplexer according to claim 2,
~~characterized in thatwherein~~ the at least one filter head (FHD1, FHD2, ... FHD5) further
comprises a second coupling.

4. (Currently Amended) A Reconfigurable multiplexer according to claim 1,
~~characterized in thatwherein~~ the at least one filter head (FHD1, FHD2, ... FHD5) is an integral
part of the manifold (MF).

5. (Currently Amended) A Reconfigurable multiplexer according to claim 1,
~~characterized in thatwherein~~ the covering plate (SC1, SC2, ... SC5) is at a distance (—) with
respect to the manifold axis.

6. (Currently Amended) A method for providing a reconfigurable multiplexer for a
wireless ~~transceiver~~transceiver comprising:
providing a manifold (MF); and
providing filter ports and filter means, with each filter means being to be connected to the
manifold (MF) ~~at a corresponding one of said portsproper locations (P1, P2, ... P5),~~
~~characterized in thatwherein~~ the step of providing filter means comprises providing at
least one filter comprising a first resonant cavity and a further resonant cavity, and at least one
filter head (FHD1, FHD2, ... FHD5) having only a single resonant cavity which is the same as
the first resonant cavity of said at least one filter, wherein said at least one filter head is
configured as to be connectable either to a corresponding covering plate (SC1, SC2, ... SC5) for

short circuit purposes or to a filter tail (FTL1, FTL2, ... FTL5) in order to provide full filter functionality.

7. (Currently Amended) A Mmethod according to claim 6, characterized in thatwherein the step of providing said at least one filter head (FHD1, FHD2, ... FHD5) comprises the step of providing at least one filter head comprising at least a first coupling and in addition to said first resonant cavity.

8. (Currently Amended) A Mmethod according to claim 7, characterized in thatwherein said the step of providing at least one filter head (FHD1, FHD2, ... FHD5) further comprises the step of providing at least one filter head comprising a second coupling.

9. (Currently Amended) A Mmethod according to claim 6, characterized in thatwherein the step of providing the at least one filter head (FHD1, FHD2, ... FHD5) comprises the step of forming such at least one filter head asis an integral part of the manifold (MF).

10. (Currently Amended) A Mmethod according to claim 9, characterized in thatwherein the at least one filter head is made through standard waveguide technology, preferably H-plane and the corresponding at least one filter tail is made either by a technology selected from the group consisting of H-plane technology or by and DR technology to make the device more compact.

11. (Currently Amended) A~~B~~branching unit comprising one or more reconfigurable multiplexers according to claim 1.